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APPLICANTS:

Fitzgerald et al.

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EXAMINER: Walter Lee Lindsay, Jr.

TITLE:

GATE TECHNOLOGY FOR STRAINED SURFACE CHANNEL AND

STRAINED BURIED CHANNEL MOSFET DEVICES

Commissioner for Patents Washington, D.C. 20231

AMENDMENT AND RESPONSE

This paper responds to the Office Action mailed from the U.S. Patent and Trademark Office on May 22, 2002. Applicants also submit herewith a petition for a three-month extension of time up to and including November 22, 2002, and a check for the appropriate fee. Applicants believe that no additional fee is due for this Amendment and Response to be considered. However, please consider this a conditional petition for the proper extension, if one is required, and a conditional authorization to charge any related extension fees or other fees necessary for entry of this paper to Deposit Account No. 20-0531.

In the Claims

Please amend claims 1, 3, 5, 10-18, 20, 22-23, 26, 32-34, and 36 as set forth in the attached marked-up version of the amended claims to read as set forth in the clean copy of all pending claims. Please cancel claims 2, 4, 6-9, 19, 21, 24-25, 27-31, 35, and 37-39. Please add new claims 40-44. Support for the new claims is found in the claims as originally filed. No new matter has been added.

After entry of this Amendment and Response claims 1, 3, 5, 10-18, 20, 22-23, 26, 32-34, 36, and 40-44 will be pending in the application. In accord with 37 C.F.R. § 1.121, attached is a clean copy of all pending claims and a copy of a marked up version of the amended claims.